

L Numb r	Hits	Search Text	DB	Tim stamp
-	1146	(307/66).CCLS.	USPAT	2003/07/21 14:21
-	85	(memory n ar11 volatil) and ((307/66).CCLS.)	USPAT	2003/07/21 14:33
-	330	(scheduled planned) near11 (cease cessation shut\$1down)	USPAT	2003/07/21 14:40
-	1238	transfer\$3 near11 (eraseable volatile) near11 memory	USPAT	2003/07/21 14:44
-	0	((scheduled planned) near11 (cease cessation shut\$1down)) same (transfer\$3 near11 (eraseable volatile) near11 memory)	USPAT	2003/07/21 14:44
-	0	((scheduled planned) near11 (cease cessation shut\$1down)) and (transfer\$3 near11 (eraseable volatile) near11 memory)	USPAT	2003/07/21 14:44
-	3	((307/66).CCLS.) and (transfer\$3 near11 (eraseable volatile) near11 memory)	USPAT	2003/07/21 14:44
-	16	5241508.URPN.	USPAT	2003/07/21 15:13
-	0	(transfer\$3 near11 (eraseable volatile) near11 memory) and (battery near11 below near11 disconnect\$3 near11 voltage)	USPAT	2003/07/21 15:22
-	17	(transfer\$3 near11 (eraseable volatile) near11 memory) and (battery near11 below near11 voltage)	USPAT	2003/07/21 15:53
-	155	(transfer\$3 near11 (eraseable volatile) near11 memory) and (below near11 voltage)	USPAT	2003/07/21 15:53
-	138	((transfer\$3 near11 (eraseable volatile) near11 memory) and (below near11 voltage)) not ((transfer\$3 near11 (eraseable volatile) near11 memory) and (battery near11 below near11 voltage))	USPAT	2003/07/22 10:01
-	133	rios.xa.	USPAT	2003/07/22 10:18
-	23	("4321572" "4529956" "4754180" "4901069" "5055775" "5229652" "5296830" "5315279" "5347256" "5438496" "5442956" "5498911" "5594176" "5810606" "5821632" "5856710" "5945744" "5994788" "6045527" "6147416" "6175461" "6189919" "6388548").PN.	USPAT	2003/07/22 10:03
-	7	disconnect\$3 near11 (alternate backup) near11 voltage near11 below	USPAT	2003/07/22 10:23
-	420	memory same ((battery back\$1up alternate auxiliary) near11 voltage near11 below)	USPAT	2003/07/22 11:21
-	56	(memory same ((battery back\$1up alternate auxiliary) n ar11 v ltag n ar11 b l w)) same volatil	USPAT	2003/07/22 11:19
-	113	(plann d schedul) n ar11 p w r near11 (ff utag)	USPAT	2003/07/22 11:20
-	588	memory sam ((batt ry back\$1up alt rnat auxiliary) near11 v ltag) sam v latile	USPAT	2003/07/22 11:23

-	0	((planned sch dul) n ar11 p w r near11 (off utag)) and (m m ry same ((batt ry back\$1up alternate auxiliary) n ar11 voltage) same v latil)	USPAT	2003/07/22 11:23
-	3	((plann d sch dul) near11 p w r n ar11 (ff utag)) and (transf r with mem ry)	USPAT	2003/07/22 11:23
-	2262	battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)	USPAT	2003/07/30 15:02
-	577	(battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) near11 (switch\$3 off disconnect\$3 disabl\$3)	USPAT	2003/07/30 15:26
-	28	((battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) near11 (switch\$3 off disconnect\$3 disabl\$3)) same (dead drain\$4)	USPAT	2003/07/30 15:53
-	128	(battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) same (dead drain\$4)	USPAT	2003/07/30 15:34
-	16	((battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) same (dead drain\$4)) same memory	USPAT	2003/07/30 16:34
-	10	((battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) near11 (switch\$3 off disconnect\$3 disabl\$3)) same latch\$3	USPAT	2003/07/30 16:30
-	76	(battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) same latch\$3	USPAT	2003/07/30 16:30
-	162	(battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) same memory	USPAT	2003/07/30 16:35
-	1	((battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) same memory) same latch\$3	USPAT	2003/07/30 16:37
-	5592	latch\$3 near11 (switch\$3 transistor) near11 (off disconnect\$3 disabl\$3)	USPAT	2003/07/30 16:38
-	3	(battery near11 voltage near11 below near11 (minimum predetermined predefined acceptable)) same (latch\$3 near11 (switch\$3 transistor) near11 (off disconnect\$3 disabl\$3))	USPAT	2003/07/30 16:53
-	10	((("5241508") or ("5799200") or ("5359569") or ("5590340") or ("5828823") or ("5889933") or ("6473355") or ("5944828") or ("6079026") r ("6362541"))).PN.	USPAT	2003/07/30 16:54

L Number	Hits	Search Text	DB	Tim stamp
-	72	(disconn ct\$3) near2 (auxiliary alt mative alt mat stand\$1by m rgency) n ar2 batt ry	USPAT	2003/09/09 14:16
-	0	((disc nn ct\$3) n ar2 (auxiliary alternativ alternate stand\$1by emergency) near2 battery) same (volatile adj memory)	USPAT	2003/09/09 14:15
-	258	(disconnect\$3 decoupl\$3 switch\$3) near2 (auxiliary alternative alternate stand\$1by emergency) near2 battery	USPAT	2003/09/09 14:15
-	3	((disconnect\$3 decoupl\$3 switch\$3) near2 (auxiliary alternative alternate stand\$1by emergency) near2 battery) same (volatile adj memory)	USPAT	2003/09/09 14:15
-	55	((auxiliary alternative alternate stand\$1by emergency) near2 battery) same (volatile adj memory)	USPAT	2003/09/09 14:18
-	29266	(battery cell) near5 (drain overdischarg\$3 draining drainage excess excessive)	USPAT	2003/09/09 14:20
-	7908	((battery cell) near5 (drain overdischarg\$3 draining drainage excess excessive)) same memory	USPAT	2003/09/09 14:20
-	558	((battery cell) near5 (drain overdischarg\$3 draining drainage excess excessive)) same (volatile adj memory)	USPAT	2003/09/09 14:21
-	2026	((battery cell) near5 (drain overdischarg\$3 draining drainage excess excessive)) near11 (switch\$3 disconnect\$3 disabl\$3 deactivat\$3 decoupl\$3)	USPAT	2003/09/09 14:21
-	20	((battery cell) near5 (drain overdischarg\$3 draining drainage excess excessive)) near11 (switch\$3 disconnect\$3 disabl\$3 deactivat\$3 decoupl\$3)) same (volatile adj memory)	USPAT	2003/09/09 14:21
-	15	latch\$3 near11 switch\$3 near11 (back\$1up alternate auxiliary) near11 battery	USPAT	2003/09/11 20:26
-	8	("4316248" "4485456" "4645943" "4730121" "4845419" "5065091" "5280230" "5488283").PN.	USPAT	2003/09/11 17:49
-	124	latch\$3 near11 data near11 (integrity corrupt\$3)	USPAT	2003/09/11 20:28
-	7	(latch\$3 near11 data near11 (integrity corrupt\$3)) near11 (processor processing microprocessor microprocessing)	USPAT	2003/09/11 20:36
-	142	(714/14).CCLS.	USPAT	2003/09/11 20:39
-	4476	(auxiliary stand\$1by alternate) near3 battery	USPAT	2003/09/11 20:40
-	13	((714/14).CCLS.) and ((auxiliary stand\$1by alternate) near3 batt ry)	USPAT	2003/09/11 20:40